

TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED / ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		ATTORNEY'S DOCKET NUMBER P67265US0
		US APPLICATION NO. (known, see 37 CFR 1.5) 09/926494
INTERNATIONAL APPLICATION NO. PCT/GB00/01748	INTERNATIONAL FILING DATE 12 May 2000	PRIORITY DATE CLAIMED 12 May 1999
TITLE OF INVENTION LIGHT EMITTING DIODE WITH IMPROVED EFFICIENCY		
APPLICANT(S) FOR DO/EO/US Ifor David William SAMUEL, John Mark LUPTON, Benjamin James MATTERSON, William Leslie BARNES and Martin Guy SALT		

Applicant herein submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information.

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).
4. ☒ A proper Demand for Internatl. Preliminary Examination was made by the 19th month from earliest claimed priority date.
5. ☒ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
 - a. ☒ is transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☒ has been transmitted by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US)
6. ☐ A translation of the International Application into English (35 U.S.C. 371(c)(2)).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))
 - a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☐ have been transmitted by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☒ have not been made and will not be made.
8. ☐ A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☐ A translation of the annexes to the Internatl. Preliminary Examination report under PCT Article 36 (35 U.S.C. 371(c)(5)).

Items 11. to 16. below concern other document(s) or information included:

11. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
12. ☐ An assignment document for recording. A separate cover sheet compliance with 37 CFR 3.28 and 3.31 is included.
13. ☒ A **FIRST** preliminary amendment.
☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
14. ☐ A substitute specification.
15. ☐ A change of power of attorney and/or address letter.
16. ☒ Other items or information:
 - International Search Report - EPO
 - PCT Request Form
 - PCT/IB/304 Form
 - First Page of Publication
 - Demand
 - International Preliminary Examination Report - with annexes

US APPLICATION NO. (If known, see 37 CFR 1.5) 09/926494		INTERNATIONAL APPLICATION NO PCT/GB00/01748		ATTORNEY'S DOCKET NUMBER P67265US0	
17. <input checked="" type="checkbox"/> The following fees are submitted: Basic National Fee (37 CFR 1.492(a)(1)-(5)): Internatl. prelim. examination fee paid to USPTO (37 CFR 1.492 (a) (1)) .. \$710.00 No international preliminary examination fee paid to USPTO (37 CFR 1.492 (a) (2)) but international search fee paid to USPTO (37 CFR 1.445(a)(2)) .. \$740.00 Neither international preliminary examination fee (37 CFR 1.492 (a) (3)) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO) \$1040.00 International preliminary examination fee paid to USPTO (37 CFR 1.492 (a) (4)) and all claims satisfied provisions of PCT Article 33(2)-(4) \$100.00 Search Report prepared by the EPO or JPO (37 CFR 1.492 (a) (5)) \$890.00 <div style="text-align: right;">ENTER APPROPRIATE BASIC FEE AMOUNT =</div>				CALCULATIONS	PTO USE ONLY
				\$ 890.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input checked="" type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(e)).				\$ 130.00	
Claims	Number Filed	Number Extra	Rate		
Total Claims	30 - 20 =	-10-	x \$18.00	\$ 180.00	
Independent Claims	2 - 3 =	-0-	x \$84.00	\$	
Multiple Dependent Claim(s) (if applicable)			+ \$280.00	\$	
TOTAL OF ABOVE CALCULATIONS =				\$ 1200.00	
Reduction by 1/2 for filing by small entity , if applicable. Verified Small Entity statement must also be filed. (Note 37 CFR 1.9, 1.27, 1.28).				\$	
SUBTOTAL =				\$ 1200.00	
Processing fee of \$130 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f))				\$	
TOTAL NATIONAL FEE =				\$ 1200.00	
Fee of \$40.00 for recording the enclosed assignment (37 CFR 1.21(h)). Assignment must be accompanied by appropriate cover sheet (37 CFR 3.28, 3.31).				\$	
TOTAL FEES ENCLOSED =				\$ 1200.00	
				Amt. to be refunded:	\$
				Amt. charged:	\$

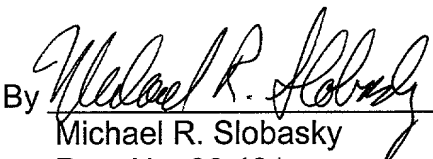
a. ☒ A check in the amount of \$ 1200.00 to cover the above fees is enclosed.

b. ☐ Please charge my Deposit Account No. 06-1358 in the amount of \$ _____ to cover the above fees. A duplicate copy of this sheet is enclosed.

c. ☒ The Commissioner is hereby authorized to charge my account any additional fees set forth in §1.492 during the pendency of this application, or credit any overpayment to Deposit Account No. 06-1358. A duplicate copy of this sheet is enclosed.

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By 
 Michael R. Slobasky
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JPH&S 3/95

09/926494

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ifor David William SAMUEL et al.

Serial No.: New

Filing Date: November 13, 2001

For: LIGHT EMITTING DIODE WITH IMPROVED EFFICIENCY

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend claims 3 through 6, 8 through 14, 17, 18, 20, 22 through 24 and 28 as follows:

3. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is adapted to modify the intensity, polarisation or spectrum of emitted light.

4. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is generally lateral, such as to extend in a substantially parallel plane to the one or more semi-conductor layers and/or further layers of the device.

5. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is specifically configured to control the polarisation state of emitted radiation.

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6. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is adapted to control the frequency of radiation emitted in a given direction.

8. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is configured in conjunction with the photonic band-structure of the LED to allow for the preferential excitation of one or more desired wave guide modes.

9. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is of microscopic scale in the order of 50-2000 nanometers, more preferably between 100 and 600 nm, more preferably between 350 and 450 nm and ideally 400nm.

10. (amended) A LED as claimed in Claim 1 wherein at least one semiconducting layer, or a component thereof, is capable of light emission by luminescence.

11. (amended) A LED as claimed in Claim 1 wherein the microstructured feature is solid such that any or all microstructured layers are continuous.

12. (amended) A LED as claimed in Claim 1 wherein the microstructured feature provides the entirety of at least one of the microstructured layers and/or electrodes.

13. (amended) A LED as claimed in Claim 1 wherein the microstructured feature acts as a diffraction grating.

14. (amended) A LED as claimed in Claim 1 wherein the microstructured feature comprises features in the form of

corrugation, which is in the form of one or more non-planar surfaces or layers and comprises an array of opposed projecting portions.

17. (amended) A LED as claimed in Claim 14 wherein the corrugation is in the entirety of the layer.

18. (amended) A LED as claimed in Claim 1 wherein the microstructured feature comprises areas of modified refractive index.

20. (amended) A LED as claimed in Claim 1 comprising at least one organic or organometallic semi-conducting layer.

22. (amended) A LED as claimed in Claim 1 comprising at least one inorganic semi-conducting layer.

23. (amended) The use of a semi-conducting organic or organometallic layer with lateral microstructure in a LED as claimed in Claim 1.

24. (amended) The use of a LED as claimed in Claim 1 as a light emitting display of any desired surface area and for portable or fixed purpose.

28. (amended) The method of Claim 25 wherein the microstructured feature is produced by embossing, photolithography, microcontact printing or laser holography or by deposition on a microstructured substrate or microstructured contact.

Please cancel omnibus claims 31 and 32 without prejudice or disclaimer.

REMARKS

The foregoing Preliminary Amendment is requested in order to delete the multiple dependent claims and avoid paying the multiple dependent claims fee.

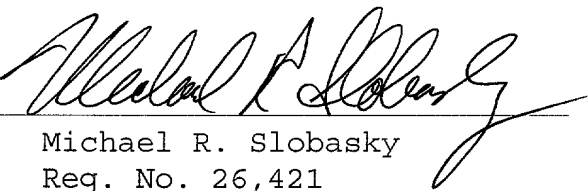
Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "**VERSION WITH MARKINGS TO SHOW CHANGES MADE.**"

Early action on the merits is respectfully requested.

Respectfully submitted,

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Atty. Docket: P67265US0
Date: November 13, 2001
MRS/cmf

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

3. (amended) A LED as claimed in Claim 1 [or Claim 2] wherein the microstructured feature is adapted to modify the intensity, polarisation or spectrum of emitted light.

4. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature is generally lateral, such as to extend in a substantially parallel plane to the one or more semi-conductor layers and/or further layers of the device.

5. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature is specifically configured to control the polarisation state of emitted radiation.

6. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature is adapted to control the frequency of radiation emitted in a given direction.

8. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature is configured in conjunction with the photonic band-structure of the LED to allow for the preferential excitation of one or more desired wave guide modes.

9. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature is of microscopic scale in the order of 50-2000 nanometers, more preferably between 100 and 600 nm, more preferably between 350 and 450 nm and ideally 400nm.

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10. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein at least one semiconducting layer, or a component thereof, is capable of light emission by luminescence.

11. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature is solid such that any or all microstructured layers are continuous.

12. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature provides the entirety of at least one of the microstructured layers and/or electrodes.

13. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature acts as a diffraction grating.

14. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature comprises features in the form of corrugation, which is in the form of one or more non-planar surfaces or layers and comprises an array of opposed projecting portions.

17. (amended) A LED as claimed in Claim 14 [any of Claims 14 to 16] wherein the corrugation is in the entirety of the layer.

18. (amended) A LED as claimed in Claim 1 [any preceding claim] wherein the microstructured feature comprises areas of modified refractive index.

20. (amended) A LED as claimed in Claim 1 [any preceding claim] comprising at least one organic or organometallic semi-conducting layer.

22. (amended) A LED as claimed in Claim 1 [any preceding claim] comprising at least one inorganic semi-conducting layer.

23. (amended) The use of a semi-conducting organic or organometallic layer with lateral microstructure in a LED as claimed in Claim 1 [any preceding claim].

24. (amended) The use of a LED as claimed in Claim 1 [any of Claims 1 to 22] as a light emitting display of any desired surface area and for portable or fixed purpose.

28. (amended) The method of Claim 25 [any of Claims 25 to 27] wherein the microstructured feature is produced by embossing, photolithography, microcontact printing or laser holography or by deposition on a microstructured substrate or microstructured contact.

LIGHT EMITTING DIODE WITH IMPROVED EFFICIENCY

The present invention relates to novel light emitting diodes (hereinafter LEDs), components and uses thereof, to a process for the production thereof and a method for light emission. More particularly the present invention relates to novel organic LEDs adapted for improved efficiency light emission, and provides a way of controlling the polarisation and spectrum of the emitted light. It relates to uses of such devices in light-emitting displays including LCDs (Liquid Crystal Displays) and the like, to a process for the production thereof and a method for light emission.

The invention has applications in light-emitting displays, such as for calculators, clocks, mobile phones, computers, information display and elsewhere. It may be used in a display in which a pattern of LEDs is employed or alternatively as a backlight for an LCD. Many further applications will be apparent to the reader familiar with LED technology and include use as an electroluminescent screen, as a light source, lit control button and the like.

Light emission from materials can occur as a result of optical or electrical stimuli. The former process is known as photoluminescence and the latter process is known as electroluminescence. Both processes are well known in a wide range of semiconductors. Electroluminescence occurs in LEDs.

LEDs are semiconducting electronic devices in which the injection of negative and positive charges (known in the art as electrons and holes respectively) leads to the emission of light from the device. The light-emitting material may be organic or inorganic.

An LED may consist of several layers or regions adapted for charge injection,

charge transport, and light-emission. Each layer or region may be an organic or inorganic material.

Semiconducting inorganic LEDs are known, employing inorganic
5 semiconducting materials for all the above charge transporting and light-emitting purposes. Light-emission occurs at a junction between p-type and n-type doped semiconducting regions. Inorganic semiconductors are generally crystalline materials with complicated (but well-established) manufacturing procedures. Gallium compounds and alloys are commonly used.

10

An organic light emitting diode (LED) consists of one or more organic layers adapted for charge transport and/or light-emission sandwiched between two charge injecting electrodes, of which one is usually transparent.

Both polymers and small conjugated organic molecules are suitable as
15 constituent layers for organic LEDs. Examples of suitable polymers include poly(*p*-phenylenevinylene) (PPV) and derivatives such as poly[2-methoxy, 5-2'-ethyl-hexyloxy]-1,4-phenylene vinylene] (MEH-PPV), polyfluorene and polypyridine. Examples of suitable small organic molecules include metal
20 chelates such as aluminium *tris*quinolate (Alq₃). The work functions of the electrodes are adapted for the injection of charge into the organic layers. Materials with relatively high work function such as indium tin oxide (ITO) or gold are suitable for hole injection, and ITO is particularly convenient because of its transparency. Materials with low work function are commonly selected
25 for electron injection, such as aluminium, calcium, lithium, magnesium, alloys and admixtures thereof.

A simple organic LED can be made by spin-coating a polymer such as MEH-PPV onto an ITO-coated substrate followed by evaporation of a top metal
30 contact. It is also known that organic layers can be deposited successfully by

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printing.

The operation of an LED involves many steps: charge injection, charge transport, recombination of opposite charges, light emission, and light escape from the device. The efficiency of an LED (which can be defined as the number of photons emitted divided by the number of charges passing through the device) depends on the fraction of injected charges which recombine with an opposite charge, the fraction of resulting recombination processes which generate light, and the fraction of the light generated in the device which escapes from the device.

In general, the light emitted from LEDs is unpolarised. However, for some applications, notably backlighting of LCDs, a source of polarised light is desirable.

A serious problem with LEDs is the trapping of light emitted by the emissive layer in waveguide modes within the substrate. This may reduce the overall external device efficiency dramatically. For example in the case of organic LEDs it has been proposed that only a fraction $1/2n^2$ of the light generated inside the LED escapes from it, where n is the refractive index of the organic layer (Angular Dependence of the Emission from Conjugated Polymer Light-emitting Diode: Implications for Efficiency Calculations, N.C. Greenham, Adv. Mat. 1994, 6, No 6).

Taking a refractive index of 2 (typical of a conjugated polymer such as PPV) would imply that seven eighths of the light generated inside the device is trapped there, and only one eighth escapes. In inorganic semiconductors the same considerations apply and refractive indices are higher (a typical value is 3.5). It has been estimated that only 3% of the light generated in a planar LED will escape (see for example D. Wood, Optoelectronic Semiconductor Devices,

Prentice Hall, Hemel Hempstead. 1994, page 86)

The present invention provides a novel way of improving LED performance by increasing and controlling the light coupling out of the device.

5

It is important to appreciate that the invention concerns the adaptation of LEDs for improved efficiency light emission, and for enabling control of the polarisation and spectrum of the emitted light. LED devices operate by spontaneous emission of radiation, in contrast to lasers which operate by stimulated emission of radiation with spatial and temporal coherence of the emitted light, a threshold current for light emission, and (usually) single-wavelength operation. LEDs are generally much simpler, cheaper and more robust than lasers, and any adaptation of LEDs for improved efficiency should therefore be similarly simple, cheap and robust.

15

Emissive species embedded in the emissive layer of an LED structure emit their energy into the available modes of the structure, these modes include: radiative modes, the ones that produce useful radiation; trapped guided modes, including waveguide modes and surface plasmon polariton modes associated with the metal contacts. In addition, emissive species (emitters) may lose their energy via non-radiative decay, for example resulting in the generation of phonons.

The different radiative modes are characterised by their frequency and in-plane wavevector. Modes having in-plane wavevectors less than free space photons of the same frequency may escape the LED structure and are responsible for the useful radiation from planar LEDs. Modes having a higher wavevector are not able to radiate into free space, these radiative modes are thus trapped (waveguided) in the layers that comprise the LED structure. These latter trapped modes may carry the majority of the energy.

Various attempts have been made to raise external device efficiency by macro-roughening the exit surface by which light leaves the device (for example US 3 739 217 and US 4 080 245). Random roughening of surfaces has been shown to
5 increase light output (R. Windisch et al. Applied Physics Letters, 74 (1999) 2256-2258).

Other methods of raising efficiency are the provision of internal channels and/or microcavities (US 4, 856 014. Control of photoluminescence emission
10 from a conjugated polymer using an optimised microcavity structure, D.G. Lidzey et al. Chemical Physics Letter 263 (1996) 655-660).

However, there still exists a need to improve the efficiency of light emission of an LED. Furthermore, none of the above approaches gives any control over the
15 polarisation of the emitted light. We have now surprisingly found that it is possible to provide a semiconducting LED meeting these requirements in admirable manner.

We have also surprisingly found that the present invention gives some control
20 of the spectrum of emitted light, and improves the electrical characteristics of the LED such that light emission starts at a lower voltage.

In its broadest aspect there is provided according to the present invention a light emitting diode (LED), comprising an electrode adapted for electron
25 injection, a second opposing electrode adapted for hole injection, one or more intermediate semi-conductor layers arranged therebetween and optionally one or more further layers wherein the device comprises at least one microstructured feature adapted to manipulate spontaneous emission or propagation of light.

The microstructured feature is suitably comprised as part of an intermediate layer, an electrode, and/or a further layer such as a surface layer which may for example be an external "cladding" layer or the like. It will be apparent that the manipulation may therefore be effective at any stage in the generation, propagation or emission of light in order to modify the properties of light emitted such as intensity, polarisation or spectrum.

The microstructured feature as hereinbefore defined may be of any form adapted to control emission and preferably comprises a substantially periodic microstructure which is effective in controlling intensity, polarisation or spectrum of the emitted light.

Without being limited to this theory, we have found that the invention provides in novel and surprising manner a mechanism by which the above requirements are met which may be in the form of Bragg scattering from substantially periodic microstructure.

The main advantages of periodic microstructure in an LED are increased efficiency, increased brightness, increased total light output, improved polarisation purity of the emitted radiation and control of the spectral variation of the emitted radiation with emission angle, as explained hereinafter.

Guided modes produced by emission from the emitters in the structure are Bragg scattered by the microstructure to produce useful radiation from the device. In this way light that would otherwise have been trapped in the emissive layer is, at least in part, recovered thus improving efficiency.

Through Bragg scattering the microstructure changes the in-plane wavevector of the guided modes. The wavevector of trapped waveguide modes may be decreased, allowing these modes to radiate. However, radiative modes may have their wavevector increased, thus trapping them in the structure. The net

result is a surprising improvement in efficiency because, as we have demonstrated, the microstructure enables more energy to be scattered out of the device than it traps.

- 5 The emission radiated from planar LED devices is in general un-polarised. With the addition of microstructure, much of the emitted radiation results from the extraction of waveguide modes by the Bragg scattering process. The polarised nature of the waveguided modes and the vectoral nature of the Bragg scattering process by which they are coupled to useful radiation means that
- 10 periodic microstructure can be used to control the polarisation state of the emitted radiation.

- As a further result of the Bragg scattering process, the frequency of the radiation emitted in a given direction is dictated by the profile of the
- 15 microstructure. Consequently, the spectral variation of the radiated output with emission angle may be controlled. The microstructured layers can consist of many regions of different periodicity to couple out light of different colours.

- In addition to the uses of periodic microstructure mentioned hereinbefore,
- 20 periodic microstructure can be used to modify the nature of the waveguided modes supported by the LED structure. By controlling the microstructure incorporated into the LED structure, in particular the periodicity thereof, the microstructure can be used to control the nature of the guided modes that the LED structure may support. This in turn allows greater control over the
- 25 radiation emitted by the LED as follows.

- Periodic microstructure, in addition to providing Bragg scattering to allow the extraction of waveguide modes, also results in the an alteration of the photonic band structure supported by layered structures [M. G. Salt and W. L. Barnes,
- 30 "Photonic Band Gaps in Guided Modes of Textured Metallic Microcavities"

Accepted for publication by Optics Communications. (accepted April 1999).]

- In particular, Bragg scattering of waveguide modes may be used to produce band gaps in the propagation of these modes. The frequency of the centre of this gap and its width (in frequency) are dictated by the profile of the microstructured interfaces together with the dielectric properties of the materials from which the layers that comprise the LED are made.
- 10 Additionally, because of the vector nature of the Bragg scattering process, the central frequency of the gap depends on the in-plane propagation direction of the waveguide mode. Importantly, if the frequency of the emission from the emitters embedded in the LED structure lies in the photonic band gap for a particular in-plane direction, the emitters are unable to generate waveguide
- 15 modes propagating in that direction. Conversely, if the frequency of the emission from the emitters embedded in the LED structure lies at one of the edges of the photonic band gap for a particular in-plane direction, the generation of waveguide modes propagating in that direction is enhanced.
- 20 Suitable tailoring of the photonic band-structure of the LED through appropriate periodic microstructure thus allows for the preferential excitation of a desired waveguide mode. This may allow for an additional increase in efficiency over and above that due to extracting guided modes by Bragg scattering. The higher mode density associated with a mode at the edge of a
- 25 photonic band gap further enhances the overall efficiency of the device by ensuring that emission by the emitters into waveguide modes preferentially produces waveguide modes that propagate in an in-plane direction that the microstructure is able to couple to useful radiation.
- 30 An additional benefit of this increased efficiency is that there is an associated

increase in the decay rate of emission from the emitters, owing to the flattened photonic bands. This may prove advantageous when high modulation rates are required, or emitters suffering from significant non-radiative decay are to be used. Further, by preferentially enhancing the emission from the emitters into waveguide modes propagating in selected in-plane directions, additional improvements in the polarisation purity of the emitted radiation can be achieved, over and above that due to extracting guided modes by Bragg scattering.

- 10 Reference herein to a layer is to a substantially uniform discrete layer of a material, the properties of which are suited to the function of the layer. Accordingly, it will be apparent that individual layers are distinguished by nature of configuration or component material, which may comprise one or a plurality of chemical entities present as a physical or chemical mixture. The electrodes may form an entire layer of the LED or may be present as discrete areas within a layer or on the surface of the LED.

Preferably, at least one semiconducting layer or a component thereof, is capable of light emission by luminescence. Preferably, the emissive layer(s) or component thereof are substantially coincident with the junction of coincidence of charge carriers. More preferably the or each semiconducting layer is substantially emissive.

Reference herein to electrons and holes is to negative and positive charge carriers respectively, as known in the art, also known as negative and positive polarons.

Reference herein to microstructure is to a structure having features and/or properties characterised by a small scale, such as microscopic. Features may be in the form of physical structuring, such as corrugation. Properties may be in

the form of micro variations in refractive index by composition variation, ion implantation or other methods and the like. Preferably, the microstructure is of microscopic scale.

5 Reference herein to substantially periodic microstructure is to microstructure at least one section of which has a substantially regular period. The periodicity of the microstructure may be uniform throughout or the microstructure may have sections with differing periodicity. Both of these may form a pattern or array.

10 The microstructure may have any suitable period or range of periods of features and/or properties to give the effect of improved efficiency. Preferably, the period is of microscopic scale. More preferably, the period is in the order of 50 – 2000 nanometers, more preferably between 100 and 600 nm. In one preferred embodiment the period is between 350 and 450 nm and ideally 400nm.

15 Preferably the microstructure is solid such that any or all microstructured layers are continuous, that is, do not comprise any form of perforation containing no material or a material of appreciably lower optical density within their structure.

20 The microstructure is preferably generally lateral, that is, extends in a substantially parallel plane to the semiconducting layer and/or further layers and/or LED structure. The microstructure may be present in any lateral extent of the defined layers or electrodes. For example, it may extend over
25 substantially all of the microstructured layer(s) or electrode(s), a substantial part thereof or a small part thereof. Preferably, the microstructure extends over a substantial part thereof, more preferably over substantially all thereof.

The microstructured layer(s) and/or electrode(s) may extend in any lateral
30 extent of the LED structure. Preferably, they extend over substantially all of the

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LED structure.

A device as hereinbefore defined may comprise any further layers of the LED, such as supporting, sealing, protective or conductive layers and the like. The
5 microstructure may be present in any or all of these further layers. Its presence may be related to the method of fabrication or selected by other criteria.

The microstructured layer(s) and or electrodes(s) may comprise a microstructure on one or both surfaces thereof or the microstructure may
10 extend throughout the device. Preferably, the microstructure provides the entirety of at least one of the microstructured layers and/or electrodes. This has advantages of ease of manufacture in view of the low thickness of the LED layers (of the order of 20-100 nm), which complicates physical manipulation within the layer or of one surface of the layer only.

15 Preferably, the microstructure acts as a diffraction grating. This can be created by properties or structures as hereinbefore defined.

In one preferred embodiment the microstructure comprises features in the form
20 of physical structuring, preferably corrugation. Preferably the corrugation is in the form of one or more non-planar surfaces or layers and comprises an array of opposed projecting portions. The projections may be of any desired shape, such as sinusoidal, rectangular, jagged, pointed or the like.

25 The array may give, for example, a sinusoidal, square or other wave form of the layer or surface such that each projecting portion extends laterally across the layer (sometimes known as one-dimensional corrugation). Alternatively, the array may be cross-corrugated to give a dimpled form of the surface such that a projecting portion is surrounded by opposed projecting portions (sometimes
30 known as two- or multi-directional corrugation).

The corrugation may be of any suitable depth to improve efficiency. Preferably, the depth between peaks and troughs is of the order 5 to hundreds of nanometers. More preferably it is between 10 and 200 nm, even more preferably between 20 and 120 nm.

The corrugation is preferably in the entirety of the layer. The thickness of a semiconducting layer in an organic LED is of the order of 100 nm. Surprisingly we have found that at the preferred depths of corrugation, the electrical and emission properties of the semiconducting layer are not detrimentally affected, despite the high ratio of corrugation depth to layer depth, which is close to 1:1.

Where the microstructure comprises modified refractive index, the portions of the layer with modified refractive index are present within the layer and are in the form of lines or areas of modified refractive index laterally across the layer. The refractive index profile in the plane of the layer modified by such microstructure may take the form of, for example, a sinusoidal, square or other waveform of the refractive index.

For organic LEDs, an electrode as hereinbefore defined adapted for electron injection is suitably comprised of any suitable electron injecting material for example as known in the art, preferably comprises any suitable metal, alloy or semi-conductor such as aluminium, lithium-aluminium, calcium or magnesium-silver, magnesium, magnesium-aluminium and the like optionally as an alloy or admixture with suitable agents.

An electrode as hereinbefore defined adapted for hole injection is suitably comprised of any suitable hole injecting material for example as known in the art, preferably comprises any suitable metal, alloy or semi-conductor such as indium tin oxide (ITO), tin oxide or other transparent conductor, PEDOT,

polyaniline or like polymer, gold and the like, optionally as an admixture with suitable agents.

A semiconducting layer as hereinbefore defined may comprise any organic (including organometallic) or inorganic semiconducting material. Preferably it comprises organic material such as small organic molecules, such as aluminium trisquinolate (Alq3), metal chelates and the like, polymers or mixtures thereof. In a preferred embodiment, the material comprises a polymer, preferably a conjugated polymer, for example PPV and derivatives such as poly(2-methoxy, 5-(2'-ethylhexyloxy) - *p*-phenylene vinylene) (MEH-PPV), or polyfluorene. Conjugated polymers combine good transport properties with strong luminescence and simple processing.

Other suitable semiconductor layers include compounds and alloys of gallium. Particularly preferred inorganic semiconducting materials are gallium arsenide and gallium nitride, and other like compound semiconducting materials which will suggest themselves to those skilled in the semiconducting art.

A device as hereinbefore defined may comprise any further supporting, sealing or protective layers and the like. Preferably a device as hereinbefore defined comprises a transparent rigid or flexible support layer such as quartz or glass or suitable synthetic equivalent such as polymeric substrates on which the device is constructed or onto which the constructed device is transferred, whereby the integrity and uniformity thereof is preserved. A first support layer may be used during the construction of the device, onto which either outer layer may be deposited as desired, and intermediate and opposing layers subsequently applied, and a second support layer to be used during use applied to either outer layer as desired. Any of these layers may be microstructured by any suitable means.

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In a further aspect of the invention there is provided the novel use of a semiconducting organic layer with lateral microstructure in a device as hereinbefore defined.

- 5 In a further aspect of the invention there is provided the use of a device as hereinbefore defined as a light emitting display of any desired surface area and for portable or fixed purpose. A display may comprise a single continuous device or a mosaic of devices depending on the size and nature thereof.

- 10 Preferably a device is used for display purposes, such as electroluminescent TV or computer screens, back lighting of liquid crystal displays such as in watches and the like, very large area displays such as public information boards in airports and the like, small displays such as for household electrical goods including calculators, washing machines and the like, flexible displays, head-up
15 (virtual reality) displays for training, entertainment purposes and the like, for example for aircraft pilot, road user training and the like; for improved efficiency applications such as for lighting of large areas, preferably by means of panelled low intensity (low brightness) wall or ceiling lighting in place of a plurality of independent lights, lighting powered by a battery source such as car
20 brake lights, lighting in constant use such as traffic lights, warning and or road signs which may be optionally flashing or otherwise active, visible or near infra red security lighting.

- Preferably a device as hereinbefore defined comprises or is adapted to be
25 associated with means for mounting and operation thereof in known manner. Devices may be used in AC or DC operation, with the hereinbefore mentioned advantages of efficiency, brightness, control of polarisation and spectrum. The increased efficiency reduces power consumption, which is of particular benefit in battery-powered equipment.

15

In a further aspect of the invention there is provided a method for the production of a device as hereinbefore defined. The device may suitably be produced by any technique as known in the art with the additional step of producing a microstructure as hereinbefore defined. The method steps are those
5 necessary to produce the device as hereinbefore defined.

Preferably a device as hereinbefore defined is produced by providing a first support layer of desired dimensions and laminating or coating this with successive layers. Preferably, metallic layers are deposited by thermal
10 evaporation, electrolytic or reactive means.

Preferably an organic semiconducting layer as hereinbefore defined is coated in a layer for example by means of spin coating, dip-coating, printing, evaporation or epitaxial growth.

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The microstructure as hereinbefore defined may be produced by any suitable method, such as embossing, photolithography, microcontact printing or laser holography. The microstructured layer(s) and/or electrode(s) may alternatively be deposited on a microstructured substrate or microstructured contact (e.g.
20 ITO).

In one preferred embodiment features, for example corrugations, are created by exposing a photoresist or other further layer to at least one laser beam and subsequently depositing the semi-conducting layer and optionally further layers
25 directly or indirectly onto the featured layer.

Preferably, the layer is exposed to an interference pattern created by a plurality of laser beams, preferably two laser beams.

30 Preferably, the layer is then cured where appropriate and is also preferably

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coated (preferably by spin coating) with further layers.

In one preferred embodiment, the microstructure is then transferred from the photoresist layer to the substrate upon which it is supported, typically the
5 transparent support to the LED structure.

In a further aspect of the invention there is provided a method for the operation of an LED as hereinbefore defined. Suitably an LED as hereinbefore defined is operated in known manner, by applying a voltage, causing a current to flow
10 through the device. Advantageously a device as hereinbefore defined may be conveniently and reliably operated by virtue of the high efficiency thereof.

The invention is now illustrated in non-limiting manner with reference to the
15 following figures and examples.

Preferred Embodiment

In the following, the manufacture, testing and key results of a solution
20 processed single layer polymer LED with lateral microstructure is described. Firstly, the device structure is electrically stable, which is counter-intuitive due to the ratio of corrugation height to overall device thickness. It is found that the device efficiency is increased by a factor of 3 to 4, the emission spectrum is significantly altered with the vibronic structure (i.e. the peaks of the
25 characteristic emission) appearing sharper. Also, the emitted light is polarised, which is an extremely significant observation.

(i) Fabrication of the grating

30 The corrugation of lateral pitch 400 nm was made by exposing the photoresist

to the interference pattern of two laser beams. The first step was to spin the photoresist (Shipley S1805) onto silica substrates to give films of thickness 500nm. The samples were then exposed to the interference pattern of two laser beams, generated using an Argon ion laser operating at 457.9 nm. Following exposure, the samples were developed, exposed to UV radiation (245 nm), and baked in air at up to 240 °C. The samples of corrugated photoresist were characterised by measurements of angle-dependent reflectivity and also by atomic force microscopy (AFM). These measurements confirmed the lateral period of 400 nm, but showed some sample to sample variation in grating depth. The peak to trough depth of grating determined by AFM was typically 80 nm.

(ii) Polymer LED structure

The device structure studied is a solution processed single layer polymer electroluminescent (EL) device containing the polymer MEH-PPV as the emissive layer. The structure is shown in Fig. 1.

A cross section showing the vertical layer structure of a device is shown in Fig. 2. Although all of the layers are shown with microstructure, this is dependent on the fabrication parameters and not necessary for functioning of the device.

As the photoresist is insulating, it is necessary to deposit a conducting anode on top of the photoresist. Gold has a work function comparable to ITO and has previously been used as a hole injecting contact (anode). A useful, i.e. relatively low absorption, gold layer should not be thicker than 20 nm and can be as thin as 8 nm. A layer of polyethylenedioxythiophene (PEDOT), which is a commercially available conductive polymer (Bayer AG, Germany), is deposited on top of the gold layer by spin-coating, and then baked for ten hours at 50 °C in vacuo. PEDOT is transparent in the visible and hence has no effect

on the emission spectrum. The emissive layer of MEH-PPV is then spin-coated onto the PEDOT layer. Under a vacuum of 5×10^{-6} mbar a thin calcium electrode is deposited which is capped by a thick aluminium electrode to prevent oxidation. Light is emitted through the silica substrate.

5

The completed device structure in this example is shown in Figures 3a and 3b. It has 8 pixels which may be addressed individually. The grating only covers one half of the substrate, so that the other half can be used as a direct comparison of both emission spectrum and efficiency.

10

(iii) Device characteristics

Fig. 4 shows the EL spectra measured of two adjacent pixel operating at the same bias and current. The peak (at 620 nm) is increased by a factor of ~3.5 and the total area of the spectrum is increased by a factor of ~5 for the corrugated LED with respect to the uncorrugated LED. The peaks on the corrugated LED are resolved much more clearly, whereas the uncorrugated emission appears broadened. This figure demonstrates that the grating gives rise to an increase in external device efficiency and the variation of the emission spectrum (improvement of colour purity).

20

In a separate experiment, the light output of two pixels on the corrugated and uncorrugated substrate was measured as a function of driving voltage. The results are shown in figure 5, and a substantial increase in the light output is obtained for the corrugated substrate. A standard analysis by taking into account the device current yields an external efficiency greater by a factor of ~2.5 for this example.

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Fig. 6 shows the same graph on a logarithmic scale. It is clearly seen that both curves have a similar functional dependence on bias, indicating that a common electrical process gives rise to the electroluminescence (i.e. the injecting

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properties of the anode are apparently not modified significantly by the inclusion of the grating). The emission characteristic of the corrugated substrate is however increase by a factor of ~ 2.5 with respect to the uncorrugated substrate. The ratio between the two curves is essentially constant between 4 and 10 V, which suggests the enhancement is indeed purely due to the effect of the grating on scattering out waveguide modes. The discontinuity in the characteristic of the uncorrugated substrate at 5 V is insignificant (due to the low level of light involved) and is most probably due to a minute electrical spark across the device.

Surface emitting LEDs generally emit unpolarised light. The results of an experiment to study the polarisation of the light emitted by a microstructured LED are shown in Fig. 7. The emitted light was detected through a polariser oriented either parallel or perpendicular to the direction of corrugation. A strong polarisation effect is observed for the corrugated LED. All three peaks in the electroluminescence spectrum display a polarisation dependence, the effect being strongest at the long wavelength peak. This is an extremely significant observation, as it demonstrates that polarised light emitted in the plane of the substrate is actually scattered out orthogonal to the substrate. It also demonstrates a means of fabricating a polarised EL device, which is of significant interest to LCD backlight applications.

(iv) General remarks

The corrugation is clearly visible at the top of the aluminium contacts through the entire device structure, which add up to a total thickness of ~ 400 nm. This demonstrates that the corrugation propagates throughout the LED and in particular through the polymer film, which gives rise to the efficiency enhancing scattering out of waveguide modes.

The main theoretical objection to the device structure presented in this example

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here has previously been the ratio between the emissive layer thickness and the depth of the corrugation, which are clearly comparable. It was thought that an unevenness of 80 nm would lead to a highly non-uniform field across the emissive layer of comparable thickness, which generally leads to localised heating and electrical breakdown through shorting of the device. Pinhole formation and unevenness in ITO (spikes resulting from the relatively crude sputtering process) have been a major problem in organic LEDs. In the above example, all devices were free of shorts despite the strong modulation of the anode surface profile.

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ART 34 AMDT

CLAIMS

1. A light emitting diode (LED), comprising an electrode adapted for
5 electron injection, a second opposing electrode adapted for hole
injection, one or more intermediate semi-conductor layers arranged
therebetween and optionally one or more further layers wherein the LED
comprises at least one substantially periodic microstructured feature
adapted to manipulate emission and/or propagation of light by coupling
10 non-radiative waveguide-modes to far-field radiation.
2. A LED as claimed in claim 1 wherein the substantially periodic
microstructured feature is configured to increase efficiency of emission
by facilitating the coupling, at least in part to useful far-field radiation so
15 recovering some of the energy that would otherwise have been lost to
non-radiative waveguide-modes.
3. A LED as claimed in Claim 1 or Claim 2 wherein the microstructured
feature is adapted to modify the intensity, polarisation or spectrum of
20 emitted light.
4. A LED as claimed in any preceding claim wherein the microstructured
feature is generally lateral, such as to extend in a substantially parallel
plane to the one or more semi-conductor layers and/or further layers of
25 the device.
5. A LED as claimed in any preceding claim wherein the microstructured
feature is specifically configured to control the polarisation state of
emitted radiation.

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6. A LED as claimed in any preceding claim wherein the microstructured feature is adapted to control the frequency of radiation emitted in a given direction.

5 7. A LED as claimed in Claim 6 wherein the periodic microstructure consists of many regions of different periodicity to couple out light of different colours.

10 8. A LED any preceding claim wherein the microstructured feature is configured in conjunction with the photonic band-structure of the LED to allow for the preferential excitation of one or more desired wave guide modes.

15 9. A LED any preceding claim wherein the microstructured feature is of microscopic scale in the order of 50 – 2000 nanometers, more preferably between 100 and 600 nm, more preferably between 350 and 450 nm and ideally 400nm.

20 10. A LED as claimed in any preceding claim wherein at least one semi-conducting layer, or a component thereof, is capable of light emission by luminescence.

25 11. A LED as claimed in any preceding claim wherein the microstructured feature is solid such that any or all microstructured layers are continuous.

30 12. A LED as claimed in any preceding claim wherein the microstructured feature provides the entirety of at least one of the microstructured layers and/or electrodes.

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13. A LED as claimed in any preceding claim wherein the microstructured feature acts as a diffraction grating.

14. A LED as claimed in any preceding claim wherein the microstructured feature comprises features in the form of corrugation, which is in the form of one or more non-planar surfaces or layers and comprises an array of opposed projecting portions.

15. A LED as claimed in Claim 14 wherein the depth between peaks and troughs is of the order five to hundreds of nanometers.

16. A LED as claimed in Claim 15 wherein the depth is between 10 and 200 nm, more preferably between 20 and 120 nm.

17. A LED as claimed in any of Claims 14 to 16 wherein the corrugation is in the entirety of the layer.

18. A LED as claimed in any preceding claim wherein the microstructured feature comprises areas of modified refractive index.

19. A LED as claimed in Claim 18 wherein the portions of the layer with modified refractive index are present within the layer and are in the form of lines or areas of modified refractive index laterally across the layer.

20. A LED as claimed in any preceding claim comprising at least one organic or organometallic semi-conducting layer.

21. A LED as claimed in Claim 20 wherein the organic semi-conducting layer comprises a conjugated polymeric material.

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22. A LED as claimed in any preceding claim comprising at least one inorganic semi-conducting layer.

23. The use of a semi-conducting organic or organometallic layer with lateral microstructure in a LED as claimed in any preceding claim.

24. The use of a LED as claimed in any of Claims 1 to 22 as a light emitting display of any desired surface area and for portable or fixed purpose.

25. A method for the production of a light emitting diode, wherein a laminar structure is fabricated comprising an electrode adapted for electron injection, a second opposing electrode adapted for hole injection, one or more intermediate semi-conductor layers placed therebetween, and optionally one or more further layers, and further comprising the step of adapting the LED such that there is at least one substantially periodic microstructured feature adapted to manipulate emission and/or propagation of light by coupling non-radiative waveguide-modes to far-field radiation.

26. The method of Claim 25 wherein the adapting step comprises incorporating at least one semi-conducting organic layer with lateral periodic microstructure of suitable period to facilitate the coupling, at least in part to useful far-field radiation so recovering some of the energy that would otherwise have been lost to non-radiative waveguide-modes.

27. The method of Claim 26 wherein the semi-conducting organic layer is coated in a layer by means of spin coating, dip-coating, printing, evaporation or epitaxial growth.

25

28. The method of any of Claims 25 to 27 wherein the microstructured feature is produced by embossing, photolithography, microcontact printing or laser holography or by deposition on a microstructured substrate or microstructured contact.

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29. The method of Claim 28 wherein microstructured features are created by exposing a photoresist or other further layer to at least one laser beam.

10

30. The method of Claim 29 wherein the microstructure is then transferred from the photoresist layer to the substrate upon which it is supported, typically the transparent support to the LED structure.

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31. A LED adapted for light emission substantially as hereinbefore described with reference to the accompanying drawings.
32. A method for the production of a LED adapted for light emissions substantially as hereinbefore described with reference to the accompanying drawings.

1 / 7

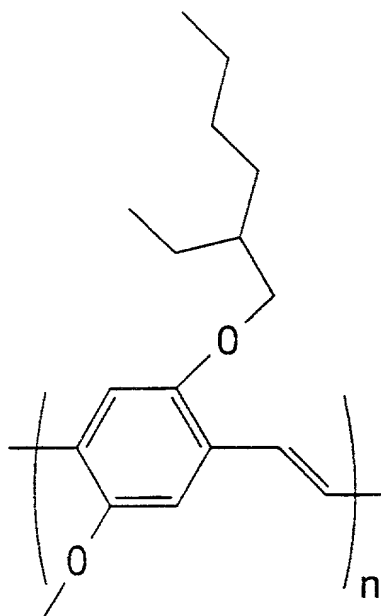


Fig. 1

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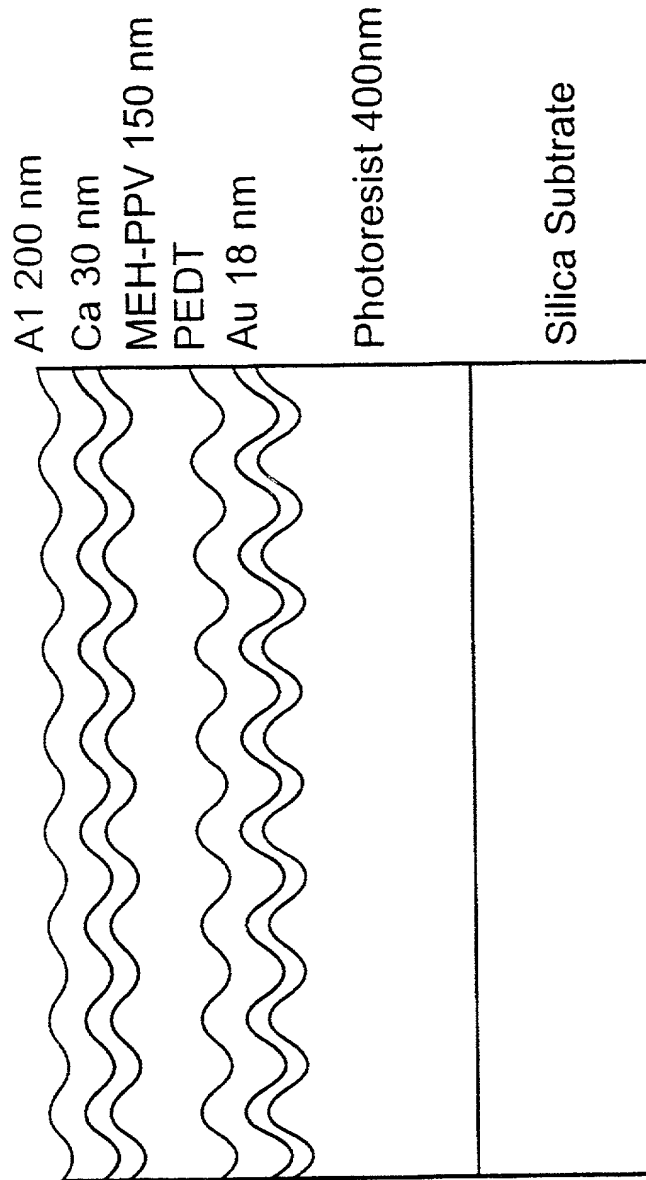


Fig. 2

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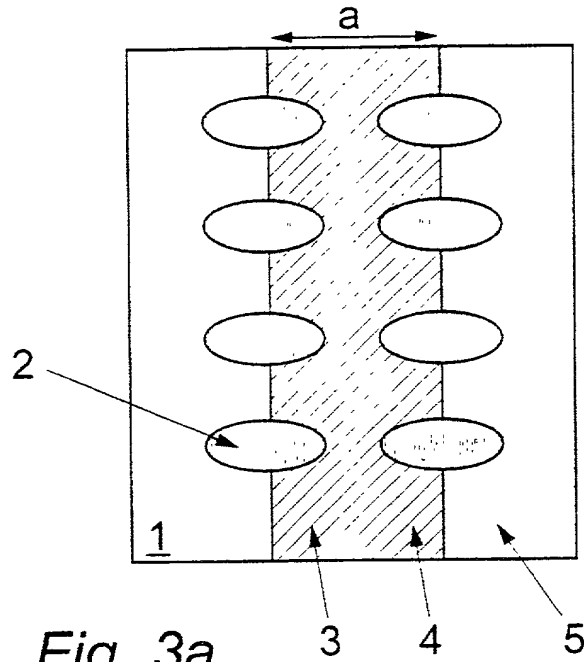


Fig. 3a

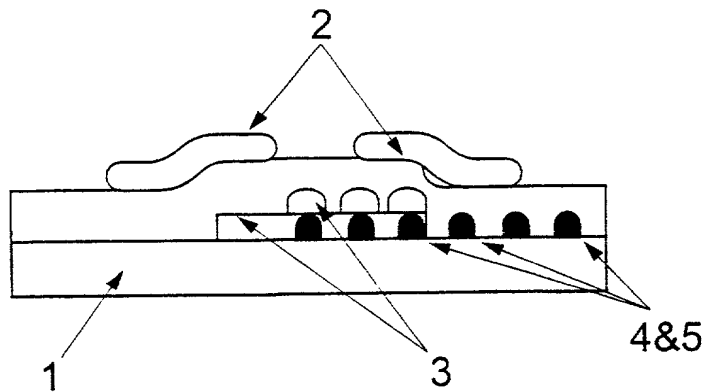
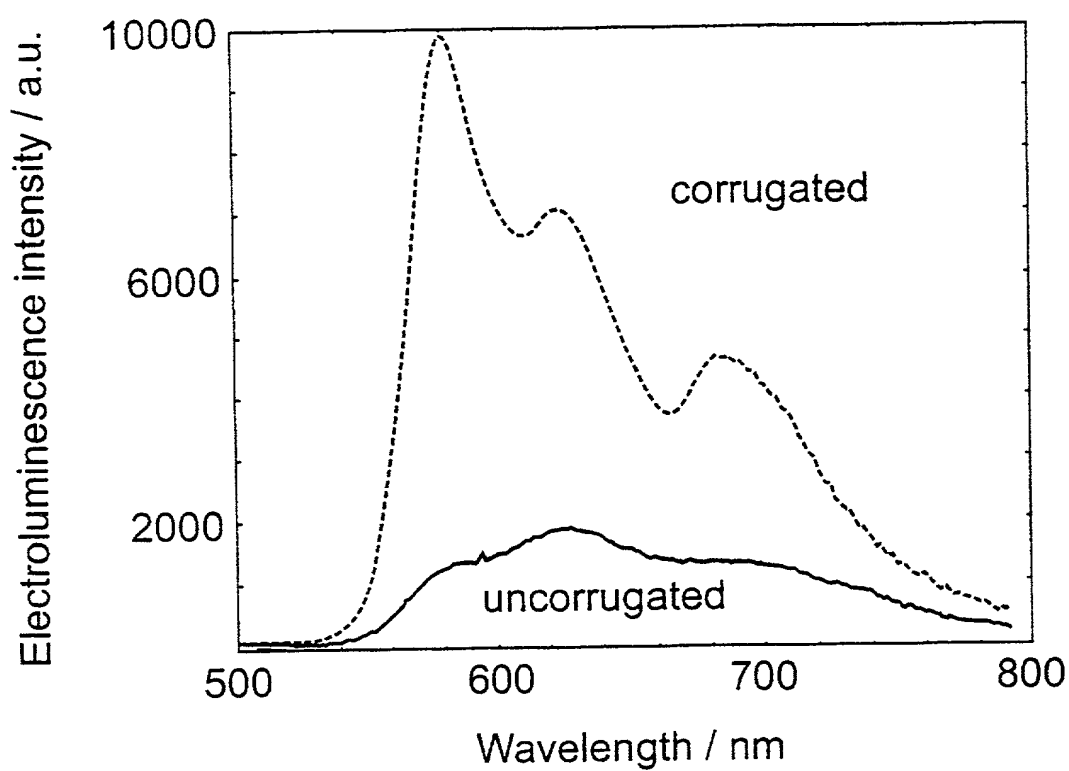
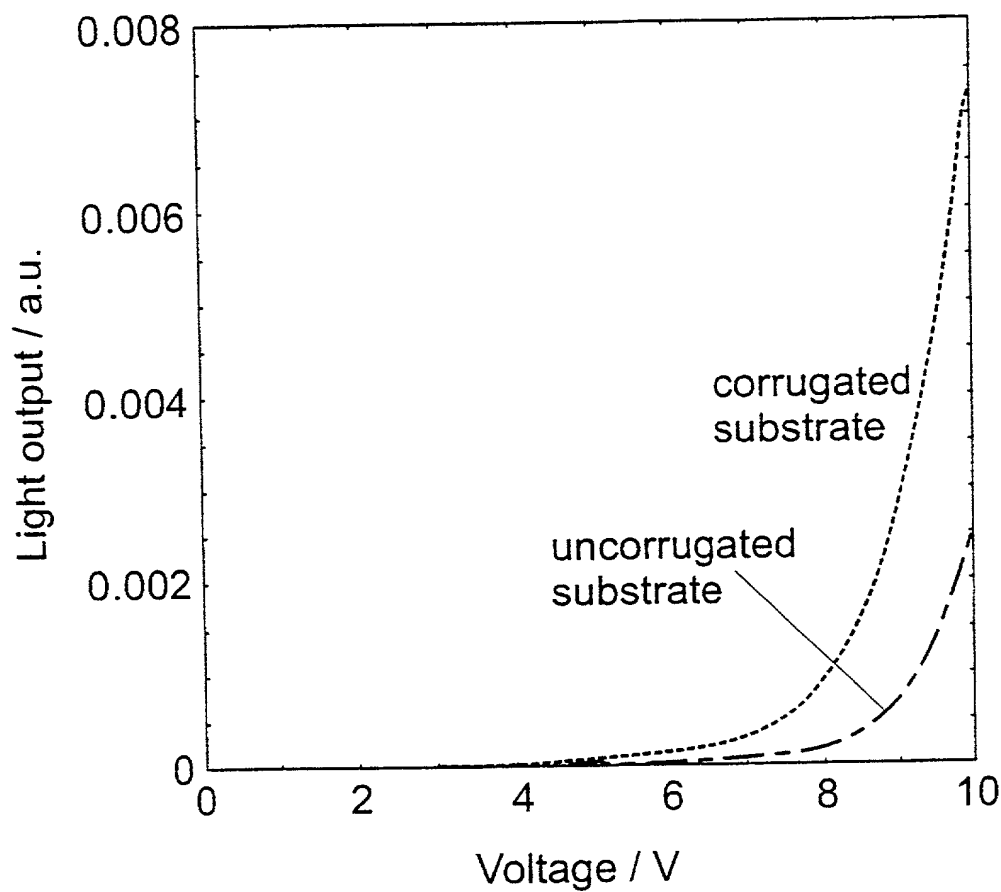


Fig. 3b

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*Fig. 4*

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*Fig. 5*

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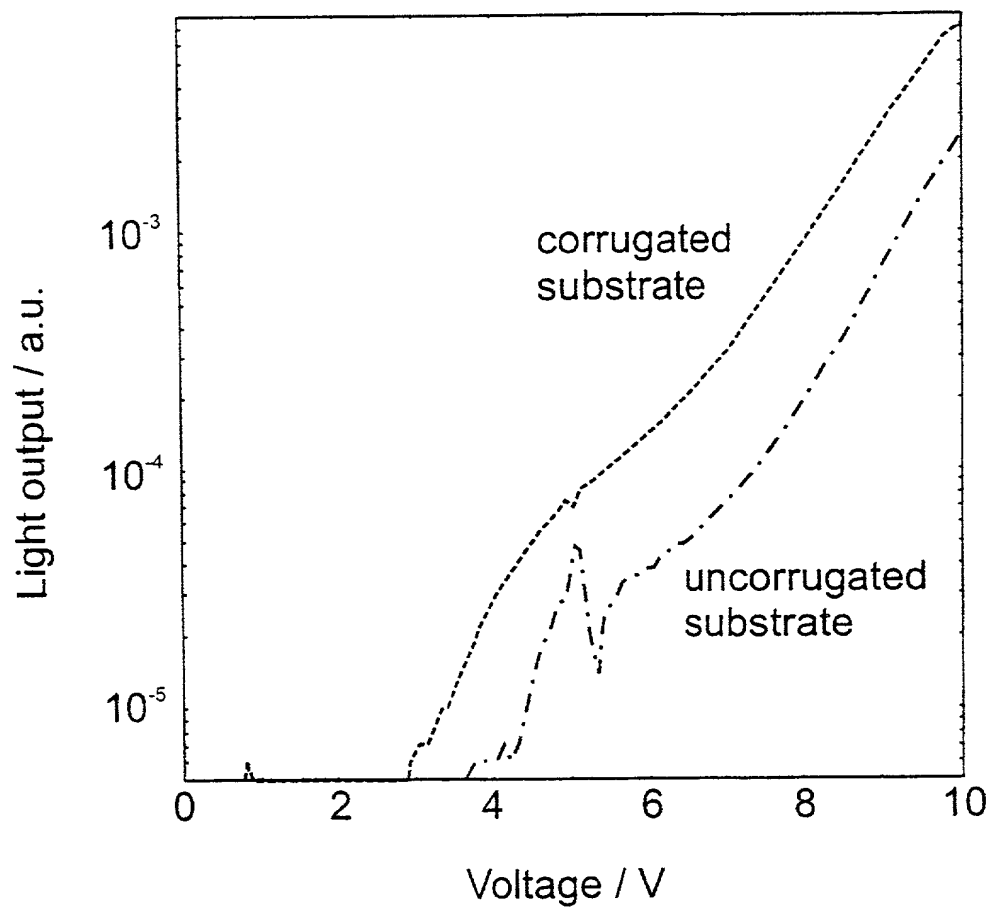
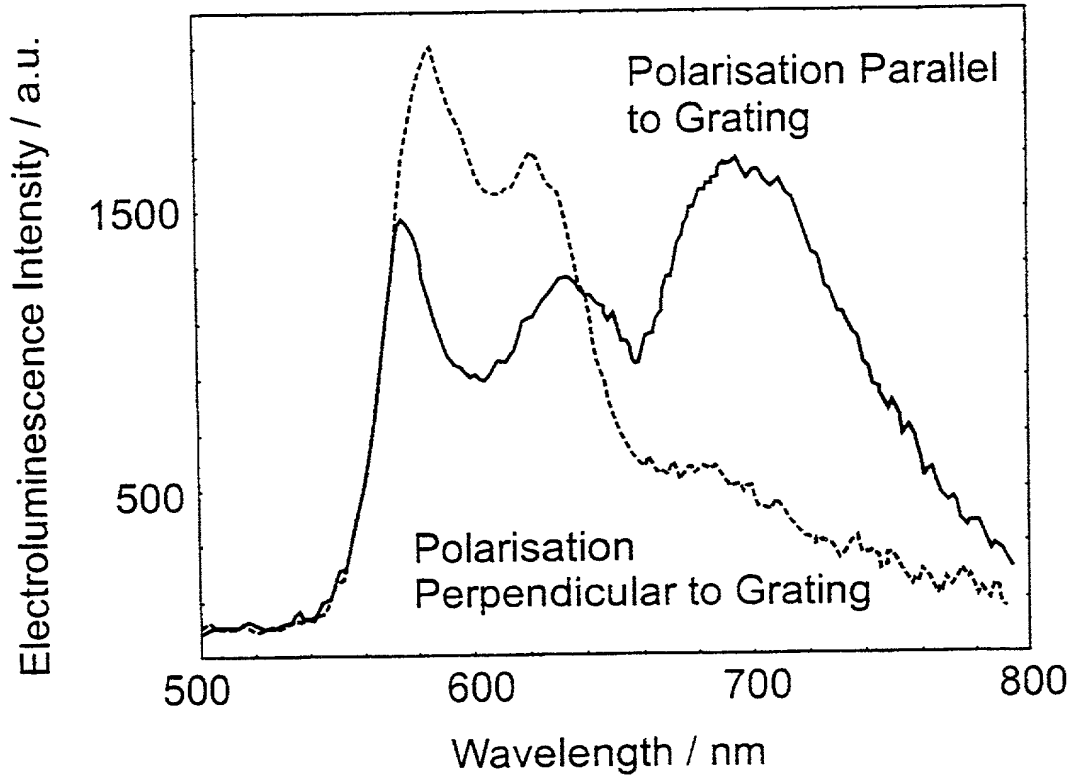


Fig. 6

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*Fig. 7*

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As a below named inventor, I declare that my residence, post office address and citizenship are stated below next to my name, the information given herein is true, that I believe that I am the original first and sole inventor (if only one name is listed at 201 below), or an original, first and joint inventor (if plural inventors are named below at 201-203, or on additional sheets attached hereto) of the subject matter which is claimed and for which patent is sought on the invention entitled:

LIGHT EMITTING DIODE WITH IMPROVED EFFICIENCY

which is described and claimed in: ☒ PCT International Application No. PCT/GB00/01748 filed May 12, 2000
☐ the attached specification ☒ the specification in application Serial No. 09/926,494 filed November 13, 2001

(if applicable) and amended on

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1 56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 (a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

9910901.9 GREAT BRITAIN 12 May 1999
(Number) (Country) (Day/Month/Year Filed)

Priority Claimed

☒ Yes ☐ No

(Number) (Country) (Day/Month/Year Filed)

☐ Yes ☐ No

(Number) (Country) (Day/Month/Year Filed)

☐ Yes ☐ No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below:

Application No. Filing Date Application No. Filing Date

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1 56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys (Registration No.) to prosecute this application, receive and act on instructions from my agent, and transact all business in the Patent and Trademark Office connected therewith: HARVEY B. JACOBSON, JR. (20,851); JOHN CLARKE HOLMAN (22,769); MARVIN R. STERN (20,640); ALLEN S. MELSER (27,215); MICHAEL R. SLOBASKY (28,421); JONATHAN L. SCHERER (29,851); IRWIN M. AISENBERG (19,007); WILLIAM E. PLAYER (31,409); YOON S. HAM (45,307) and NATHANIEL A. HUMPHRIES (22,772)

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	POST OFFICE ADDRESS	POST OFFICE ADDRESS University of Durham, Department of Physics, South Road	CITY Durham	STATE OR COUNTRY GREAT BRITAIN
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I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under section 1001 of Title 18 of the United States Code; and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SIGNATURE OF INVENTOR 201*	SIGNATURE OF INVENTOR 202*	SIGNATURE OF INVENTOR 203*
<i>X</i> <i>David Samuel</i>	<i>X</i>	<i>X</i>
DATE <i>13 Dec 2001</i>	DATE	DATE

☒ Additional inventors are named on separately numbered sheets attached hereto.

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As a below named inventor, I declare that my residence, post office address and citizenship are stated below next to my name, the information given herein is true, that I believe that I am the original first and sole inventor (if only one name is listed at 201 below), or an original, first and joint inventor (if plural inventors are named below at 201-203, or on additional sheets attached hereto) of the subject matter which is claimed and for which patent is sought on the invention entitled

LIGHT EMITTING DIODE WITH IMPROVED EFFICIENCY

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filed May 12, 2000

☐ the attached specification

☒ the specification in application Serial No 09/926,494

filed November 13, 2001

(if applicable) and amended on _____

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I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

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Prior Foreign Application(s)

Priority Claimed

9910901.9

GREAT BRITAIN

12 May 1999

☒

☐

(Number)

(Country)

(Day/Month/Year Filed)

Yes

No

(Number)

(Country)

(Day/Month/Year Filed)

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(Number)

(Country)

(Day/Month/Year Filed)

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I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below

Application No _____

Filing Date _____

Application No _____

Filing Date _____

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No)

(Filing Date)

(Status: patented, pending, abandoned)

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				ZIP CODE

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under section 1001 of Title 18 of the United States Code; and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon

SIGNATURE OF INVENTOR 201*	SIGNATURE OF INVENTOR 202*	SIGNATURE OF INVENTOR 203*
DATE	DATE	DATE

☒ Additional inventors are named on separately numbered sheets attached hereto.

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ALL PATENTS, INCLUDING DESIGN
FOR APPLICATION BASED ON PCT; PARIS CONVENTION,
NON PRIORITY; OR PROVISIONAL APPLICATIONS

FOR ATTORNEYS' USE ONLY

ATTORNEYS' DOCKET NO

P67265US0

As a below named inventor, I declare that my residence, post office address and citizenship are stated below next to my name, the information given herein is true, that I believe that I am the original, first and sole inventor (if only one name is listed at 201 below), or an original, first and joint inventor (if plural inventors are named below at 201-203, or on additional sheets attached hereto) of the subject matter which is claimed and for which patent is sought on the invention entitled

LIGHT EMITTING DIODE WITH IMPROVED EFFICIENCY

which is described and claimed in: ☒ PCT International Application No **PCT/GB00/01748** filed **May 12, 2000**
☐ the attached specification ☒ the specification in application Serial No. **09/926,494** filed **November 13, 2001**
(if applicable) and amended on _____

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above
I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56
I hereby claim foreign priority benefits under Title 35, United States Code, §119 (a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
9910901.9	GREAT BRITAIN	12 May 1999	<input checked="" type="checkbox"/> Yes	<input type="checkbox"/> No
(Number)	(Country)	(Day/Month/Year Filed)		
_____	_____	_____	<input type="checkbox"/> Yes	<input type="checkbox"/> No
(Number)	(Country)	(Day/Month/Year Filed)		
_____	_____	_____	<input type="checkbox"/> Yes	<input type="checkbox"/> No
(Number)	(Country)	(Day/Month/Year Filed)		

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

Application No. _____ Filing Date _____ Application No. _____ Filing Date _____

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No) _____ (Filing Date) _____ (Status, patented, pending, abandoned) _____

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys (Registration No.) to prosecute this application, receive and act on instructions from my agent, and transact all business in the Patent and Trademark Office connected therewith. HARVEY B. JACOBSON, JR. (20,851), JOHN CLARKE HOLMAN (22,769), MARVIN R. STERN (20,640); ALLEN S. MELSER (27,215), MICHAEL R. SLOBASKY (26,421), JONATHAN L. SCHERER (29,851); IRWIN M. AISENBERG (19,007); WILLIAM E. PLAYER (31,409), YOON S. HAM (45,307) and NATHANIEL A. HUMPHRIES (22,772)

SEND CORRESPONDENCE TO: **CUSTOMER NO. 00136**

or

JACOBSON HOLMAN
PROFESSIONAL LIMITED LIABILITY COMPANY
400 SEVENTH STREET, N.W.
WASHINGTON, D.C. 20004

DIRECT TELEPHONE CALLS TO:

(please use Attorney's Docket No.) (202) 638-6666

JACOBSON HOLMAN
PROFESSIONAL LIMITED LIABILITY COMPANY

*Inventor(s) name must include at least one unabbreviated first or middle name.

201	FULL NAME * OF INVENTOR	FAMILY NAME SAMUEL	GIVEN NAME Ilfor	MIDDLE NAME David William
	RESIDENCE & CITIZENSHIP	CITY Durham	STATE OR FOREIGN COUNTRY GREAT BRITAIN	COUNTRY OF CITIZENSHIP GREAT BRITAIN
	POST OFFICE ADDRESS	POST OFFICE ADDRESS University of Durham, Department of Physics, South Road	CITY Durham	STATE OR COUNTRY GREAT BRITAIN
202	FULL NAME * OF INVENTOR	FAMILY NAME LUPTON	GIVEN NAME John	MIDDLE NAME Mark
	RESIDENCE & CITIZENSHIP	CITY Durham	STATE OR FOREIGN COUNTRY GREAT BRITAIN	COUNTRY OF CITIZENSHIP GREAT BRITAIN
	POST OFFICE ADDRESS	POST OFFICE ADDRESS University of Durham, Department of Physics, South Road	CITY Durham	STATE OR COUNTRY GREAT BRITAIN
203	FULL NAME * OF INVENTOR	FAMILY NAME MATTERSON	GIVEN NAME Benjamin	MIDDLE NAME James
	RESIDENCE & CITIZENSHIP	CITY Durham	STATE OR FOREIGN COUNTRY GREAT BRITAIN	COUNTRY OF CITIZENSHIP GREAT BRITAIN
	POST OFFICE ADDRESS	POST OFFICE ADDRESS University of Durham, Department of Physics, South Road	CITY Durham	STATE OR COUNTRY GREAT BRITAIN

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under section 1001 of Title 18 of the United States Code; and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SIGNATURE OF INVENTOR 201*	SIGNATURE OF INVENTOR 202*	SIGNATURE OF INVENTOR 203*
<i>X</i>	<i>X</i>	<i>X</i> B. J. Matterson
DATE	DATE	DATE 3rd January 2002

☒ Additional inventors are named on separately numbered sheets attached hereto.

**DECLARATION
AND POWER OF ATTORNEY
U.S.A.**

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Prior Foreign Application(s)

Priority Claimed

9910901.9 **GREAT BRITAIN** **12 May 1999**
(Number) (Country) (Day/Month/Year Filed)

☒ Yes ☐ No

(Number) (Country) (Day/Month/Year Filed)

☐ Yes ☐ No

(Number) (Country) (Day/Month/Year Filed)

☐ Yes ☐ No

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(Application Serial No.)

(Filing Date)

(Status: patented, pending, abandoned)

POWER OF ATTORNEY As a named inventor, I hereby appoint the following attorneys (Registration No.) to prosecute this application, receive and act on instructions from my agent and transact all business in the Patent and Trademark Office connected therewith. HARVEY B. JACOBSON, JR. (20,851); JOHN CLARKE HOLMAN (22,769); MARVIN R. STERN (20,640); ALLEN S. MELSER (27,215); MICHAEL R. SLOBASKY (26,421); JONATHAN L. SCHERER (29,851); IRWIN M. AISENBERG (19,007); WILLIAM E. PLAYER (31,409); YOON S. HAM (45,307) and NATHANIEL A. HUMPHRIES (22,772)

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PROFESSIONAL LIMITED LIABILITY COMPANY

*Inventor(s) name must include at least one unabbreviated first or middle name.

201	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY
202	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY
203	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under section 1001 of Title 18 of the United States Code; and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SIGNATURE OF INVENTOR 201*	SIGNATURE OF INVENTOR 202*	SIGNATURE OF INVENTOR 203*
DATE	DATE	DATE

☒ Additional inventors are named on separately numbered sheets attached hereto.

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**JACOBSON HOLMAN PLLC
ADDITIONAL INVENTORS**

* Inventor(s) name must include at least one unabbreviated first or middle name.

204	FULL NAME * OF INVENTOR	FAMILY NAME BARNES	GIVEN NAME William	MIDDLE NAME Leslie
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	POST OFFICE ADDRESS	POST OFFICE ADDRESS School of Physics, Univ. of Exeter, Physics Building, Stocker Road	CITY Exeter, Devon	STATE OR COUNTRY GREAT BRITAIN ZIP CODE EX4 4 QL
205	FULL NAME * OF INVENTOR	FAMILY NAME SALT	GIVEN NAME Martin	MIDDLE NAME Guy
	RESIDENCE & CITIZENSHIP	CITY Exeter	STATE OR FOREIGN COUNTRY GREAT BRITAIN	COUNTRY OF CITIZENSHIP GREAT BRITAIN
	POST OFFICE ADDRESS	POST OFFICE ADDRESS School of Physics Univ. of Exeter, Physics Building, Stocker Road	CITY Exeter	STATE OR COUNTRY GREAT BRITAIN ZIP CODE EX4 4QL
206	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY ZIP CODE
207	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY ZIP CODE
208	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY ZIP CODE
209	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY ZIP CODE
210	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY ZIP CODE
211	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE OR COUNTRY ZIP CODE

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under section 1001 of Title 18 of the United States Code; and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon

SIGNATURE OF INVENTOR 204 *	SIGNATURE OF INVENTOR 205 *	SIGNATURE OF INVENTOR 206 *
<i>William Barnes</i>	<i>X</i>	
DATE 13/12/2001	DATE	DATE
SIGNATURE OF INVENTOR 207 *	SIGNATURE OF INVENTOR 208 *	SIGNATURE OF INVENTOR 209 *
DATE	DATE	DATE
SIGNATURE OF INVENTOR 210 *	SIGNATURE OF INVENTOR 211 *	
DATE	DATE	

□ Additional inventors are named on separately numbered sheets attached hereto.
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**JACOBSON HOLMAN PLLC
ADDITIONAL INVENTORS**

* Inventor(s) name must include at least one unabbreviated first or middle name

204	FULL NAME * OF INVENTOR	FAMILY NAME BARNES	GIVEN NAME William	MIDDLE NAME Leslie
	RESIDENCE & CITIZENSHIP	CITY Exeter	STATE OR FOREIGN COUNTRY GREAT BRITAIN	COUNTRY OF CITIZENSHIP GREAT BRITAIN
	POST OFFICE ADDRESS	POST OFFICE ADDRESS School of Physics, Univ. of Exeter, Physics Building, Stocker Road		CITY Exeter, Devon STATE OR COUNTRY GREAT BRITAIN ZIP CODE EX4 4 QL
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	RESIDENCE & CITIZENSHIP	CITY Exeter	STATE OR FOREIGN COUNTRY GREAT BRITAIN	COUNTRY OF CITIZENSHIP GREAT BRITAIN
	POST OFFICE ADDRESS	POST OFFICE ADDRESS School of Physics Univ. of Exeter, Physics Building, Stocker Road		CITY Exeter STATE OR COUNTRY GREAT BRITAIN ZIP CODE EX4 4QL
206	FULL NAME * OF INVENTOR	FAMILY NAME	GIVEN NAME	MIDDLE NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
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	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
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	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
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SIGNATURE OF INVENTOR 204 *	SIGNATURE OF INVENTOR 205 *	SIGNATURE OF INVENTOR 206 *
<i>[Signature]</i>	<i>[Signature]</i>	
DATE	DATE 10-01-02	DATE
SIGNATURE OF INVENTOR 207 *	SIGNATURE OF INVENTOR 208 *	SIGNATURE OF INVENTOR 209 *
DATE	DATE	DATE
SIGNATURE OF INVENTOR 210 *	SIGNATURE OF INVENTOR 211 *	
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